



PATENT
Docket No.150.01170103

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):	Basceri et al.)	Group Art Unit:	Unassigned
)		
Serial No.:	10/669,384)	Examiner:	Unknown
Confirmation No.:	Unassigned)		
)		
Filed:	September 24, 2003)		
)		
For:	METHODS FOR FORMING CONDUCTIVE STRUCTURES REGARDING SAME			

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with C.F.R. §§ 1.97 *et. seq.*, the materials enclosed herewith are brought to the attention of the Examiner as possibly being of interest in connection with the above-identified patent application. Per M.P.E.P. § 609, the information cited in the present Information Disclosure Statement shall not be construed to be an admission that the information is, or is considered to be, material to patentability. Consideration of each of the documents listed on the attached 1449 form is respectfully requested. As this patent application was filed after June 30, 2003, copies of the U.S. patents and U.S. patent application publications listed on the attached 1449 form have not been submitted. Pursuant to the provisions of M.P.E.P. §609, Applicants further request that a copy of the 1449 form, marked as being considered and initialed by the Examiner, be returned with the next Official Communication.

This application is a continuation of U.S. Patent Application Serial No. 10/227,662, filed August 26, 2002, which is a division of U.S. Patent Application Serial No. 09/711,206, filed November 9, 2000, issued as U.S. Patent No. 6,534,357 on March 18, 2003. In accordance with 37 C.F.R. §1.98(d), copies of documents previously cited by or submitted to the



Information Disclosure Statement

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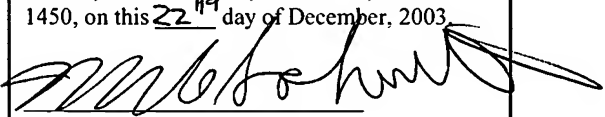
U.S. Patent and Trademark Office in connection with Applicants' prior applications listed above, are not included herewith.

It is believed that no fee is due, as this Information Disclosure Statement is filed within three months of the filing date of the application. However, in the event a fee is due, please charge any fee or credit any overpayment to Account No. 13-4895.

The Examiner is invited to contact Applicants' Representatives at the below-listed telephone number, if they can be of any assistance during prosecution of the present application.

CERTIFICATE UNDER 37 C.F.R. 1.8:

The undersigned hereby certifies that this paper is being deposited in the United States Postal Service, as first class mail, in an envelope addressed to: Assistant Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 22nd day of December, 2003.



Name: Mark J. Gebhardt

22 Dec 2003

Date

Respectfully submitted for
Basceri et al.

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INFORMATION DISCLOSURE STATEMENT	Atty. Docket No.: 150.01170103	Serial No.: 10/669,384
	Applicant(s): Basceri et al.	Confirmation No.: Unassigned
	Application Filing Date: September 24, 2003	Group: Unassigned
	Information Disclosure Statement mailed: December 22, 2003	

U.S. PATENT DOCUMENTS

Examiner Initial	Copy Enclosed	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
		5,270,241	12/14/93	Dennison et al.			
		5,555,486	09/10/96	Kingon et al.			
		6,117,689	09/12/00	Summerfelt			
		6,333,537	12/25/01	Arita			
		6,337,239	01/08/02	Dehm et al.			
		6,372,291	04/16/02	Hua et al.			
		6,482,736	11/19/02	Basceri et al.			
		6,534,357	03/18/03	Basceri et al.			
		US 2003/0001190 A1	01/02/03	Basceri et al.			

FOREIGN PATENT DOCUMENTS

Examiner Initial	Copy Enclosed	Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
		None						

OTHER DOCUMENTS (Including Authors, Title, Date, Pertinent Papers, etc.)

Examiner Initial	Copy Enclosed	Document Description
	X	McGuire, "Semiconductor Materials and Process Technology Handbook," Noyes Publ., Nowich, NY, 1988: 291.
	X	Oh et al., "Thermal Stability of RuO ₂ /Ru Bilayer Thin Film in Oxygen Atmosphere," <i>Thin Solid Films</i> , 2000;359: 118-123.
		Yoon et al., "Investigation of RuO ₂ - Incorporated PT layer as a Bottom Electrode and Diffusion Barrier for High Epsilon Capacitor Applications," <i>Electrochem. and Solid-State Lett.</i> , 2000;3(8):373-376.

EXAMINER	Date Considered
<p>*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>	